



MAULANA ABUL KALAM AZAD UNIVERSITY OF TECHNOLOGY, WEST BENGAL
(Formerly WEST BENGAL UNIVERSITY OF TECHNOLOGY)
Main Campus: NH 12, Haringhata, P.O. - Simhat, Police Station – Haringhata, Nadia-741249
City Campus: BF-142, Sector -I, Salt Lake, Kolkata -700 064

Advt. No. MAKAUT/REG/Guest Faculty/MVD/04

Dated, 08/04/2025

Applications are invited for engagement of Guest Faculty in the **Department of Microelectronics & VLSI Technology**, MAKAUT, WB with the following eligibility criteria:

I. Eligibility Criteria:

Essential: First Class Master's Degree in the appropriate branch of Engineering (Engg.)/Applied Engineering Science and Technology (Preferable Final Year project in Semiconductor Fabrication Technology) with specialization in Semiconductor Fabrication and Micro Fabrication Technology/Specialized in chip Fabrication & packaging Technology/Advanced fabrication Technology, especially epitaxial fabrication mostly preferred Silicon/compound semiconductor Fab lab.

Desirable: PhD (Tech) in Microelectronics and Technology and Fabrications and Microelectronics Device Characterization or in allied area

OR

Teaching, research industrial and / or professional experience in the relevant field mentioned above of a reputed organization.

Details of Course and Hours to be taught:

| Subject Code | Subject Name | Hour (Even Semester) | No. of Guest Faculty Required |
|--------------|--|----------------------|-------------------------------|
| UGEVT493 | Semiconductor Foundry Lab/ Introduction to Micro fabrication Lab | 24 | 02 |
| UGEVT404 | Introduction to Micro fabrication | 36 | |
| PGMVD-204 | Advanced Micro & Nano Devices | 36 | |

II. Eligibility Criteria:

Essential: First Class Master's Degree in the appropriate branch of Engineering (Engg.) / Applied Engineering Science and Technology or allied area with specialization in VLSI Process & VLSI Device simulation process simulation/Device simulation/Characterization using Industry standard TCAD tool like SILVACO, Synopsys.

Desirable:

PhD in Microelectronics Device Characterization or in allied area. OR

Teaching, research industrial and / or professional experience in a reputed organization.

Details of Course and Hours to be taught:

| Subject Code | Subject Name | Hour (Even Semester) | No. of Guest Faculty Required |
|--------------|---|----------------------|-------------------------------|
| PGMVD291 | Micro Electronics Lab-II (Characterization of semiconductor device) | 36 | 01 |

III. Eligibility Criteria:

Essential: BE/B.Tech & ME/ M.Tech in relevant branch with 1st class or equivalent either in BE/B.Tech or ME/M.Tech and PhD or equivalent in Electronics Engineering / Computer science and Engineering/Electrical Engineering/any allied area with specialization in Mixed signal VLSI Design/Testing & Verification and good academic record in preceding degrees.

Desirable: Minimum of 5 years' experience in teaching / research /industry of which 2 years post PhD experience is desirable. Knowledge in digital design, HDL languages, and design verification/testing tools.

Specialization: VLSI Testing and Verification, Logic Synthesis and Verification of Digital Analog/Mixed Signal Circuits.

Details of Course and Hours to be taught:

| Subject Code | Subject Name | Hour (Even Semester) | No. of Guest Faculty Required |
|--------------|---|-------------------------|----------------------------------|
| PGVES - 202 | VLSI Design Verification and Testing | 36 | 02 |
| PGVES - 292 | VLSI Design Verification and Testing Lab | 48 | |
| PGMVD-202 | Testing & Verification of Digital Systems | 24 | |
| PGMVD292 | VLSI Design Lab-II | 36 | |

Remuneration: ₹ 500/- per class-hour for theory class and ₹ 250/- per class-hour for practical class

Age: As per State Govt./University Rules.

How to Apply: Application must refer to this advertisement. Candidates are requested to send their CV, along with a covering letter, addressed to “*The Registrar, Maulana Abul Kalam Azad University of Technology, Haringhata, Nadia*”, to the e-mail address, makaut.mvt@gmail.com hod.mvt@gmail.com with the following subject line “**Application for Guest Faculty in the Department of Microelectronics & VLSI Technology**”.

DEADLINE FOR SUBMITTING THE APPLICATION IS: 16/04/2025

Important dates:

1. Date of Publication of Notice: 08/04/2025

2. Last date of submitting Application: 16/04/2025

3. Advertisement Notice No. : MAKAUT/REG/Guest Faculty/MVD/04/04

No TA/DA will be provided for attending the interview.

**Registrar
MAKAUT, WB**